

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SYSTEM AND METHOD FOR INTEGRATING A DIGITAL CORE WITH A SWITCH MODE POWER SUPPLY							
Application Number :								
Date :								
First Named Applicant: Mr. Andrew Roman Gizara								
Confirmation Number:								
Attorney Docket Number:								
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Mr. Andrew Roman Gizara</td><td>Andrew R. Gizara</td><td>Inventor</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Mr. Andrew Roman Gizara	Andrew R. Gizara	Inventor
Submitted By:	Elec. Sign.	Sign. Capacity						
Mr. Andrew Roman Gizara	Andrew R. Gizara	Inventor						

Documents being submitted:	Files
us-request	ISMPS-usrequ.xml us-request.dtd us-request.xsl
us-declaration	ISMPS-usdecl.xml us-declaration.dtd us-declaration.xsl
us-fee-sheet	ISMPS-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	ISMPS-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd FIG1.tif FIG3.tif FIG6.tif FIG7.tif
Comments	